### Patent 7176992

## IN UNITED STATES PATENT AND TRADEMARK OFFICE

Patent No.:

7176992

Docket No: 10010923-02

Issue Date:

Feb. 13, 2007

Patentee: John Kralik

Title

Methods of Fabricating PDLC Optical Devices

## REQUEST FOR CERTIFICATION OF CORRECTION UNDER 35 U.S.C. 255

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450 Office of Patent Publication

ATTN: Certificate of Correction Branch

It is requested that a Certificate of Correction be issued correcting printing errors appearing in the above-identified United States patent. Two copies of the text of the Certificate in the suggested form are enclosed.

Pursuant to 37 C.F.R. 1.20(a), the examiner is authorized to charge the Certificate of Correction fee of \$100.00 to the Deposit Account No. 503718

Issuance of the Certificate of Correction would correct a typographical error but neither expand nor contract the scope of the claims as properly allowed. Re-examination is not required.

The Examiner is authorized to charge any additional fees or credit overpayment to Deposit Account No. 503718

Please direct all correspondence to: Avago Technologies Limited

4380 Ziegler Road, MS: 76 Fort Collins, CO 80525

Respectfully submitted,

Date: May 6, 2008

By

Reg. No.: 54,534

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(Also Form PTO-1050)

# UNITED STATES PATENT AND TRADEMARK OFFICE

CERTIFICATE OF CORRECTION	
PATENT NO. : 7176992  APPLICATION NO.: 09635606  ISSUE DATE : Feb. 13, 2007  INVENTOR(S) : John Kralik  It is certified that an error appears or errors appear in the above-identified patent and that said Letters Paten	
is hereby corrected as shown below:	-
In Claim 20, delete "retarder with electrically tunable birefringence." and insert retarder, therefor.	

MAILING ADDRESS OF SENDER (Please do not use customer number below):

This collection of information is required by 37 CFR 1.322, 1.323, and 1.324. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 1.0 hour to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Attention Certificate of Corrections Branch, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

# Avago Technologies

**Issued Patent Proofing Form** 

Note: P = PTO Error

A = Avago Error

File#: 10010923-02

**Proofread By: Divya (12/03/2007)** 

US Serial No.: 09/635,606

US Patent No.: US 7,176,992 B1

Issue Dt.: Feb.13, 2007

Title: METHODS OF FABRICATING PDLC OPTICAL DEVICES

PR Instructions: Face Page, Abstract and Claims

Sr.No.	P/A	Original		Issued Patent		Description of Error	
		Page	Line	Column	Line		
1*	A	Page 21 Claims	Claim 22 Line 5	10	58	In Claim 20, delete "retarder with electrically tunable birefringence." and	
		(08/10/2000)			'	insert retarder, therefor.	

# \*NOTE:

In the Reference File, Notice of Allowance (dated 11/28/2006), is responsive to the Amendment File, Claims (10/02/2006), where the status of Claim 22 is given as Original, discrepancy was found while proofing the same from the Original File (i.e. Claims filed on 08/10/2000) and is being reported as Avago Error in the Issued Patent Proofing Form above.



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	Attorney Docket Number:	6536-118			Earliest Publication Date:
	Class / Subclass:	349/086			Patent Number:
	First Named Inventor:	John Kralik	t , Lansdale, P	Α	Issue Date of Patent:
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